

FIG. 14

BIC
status
registers

FIG. 16B

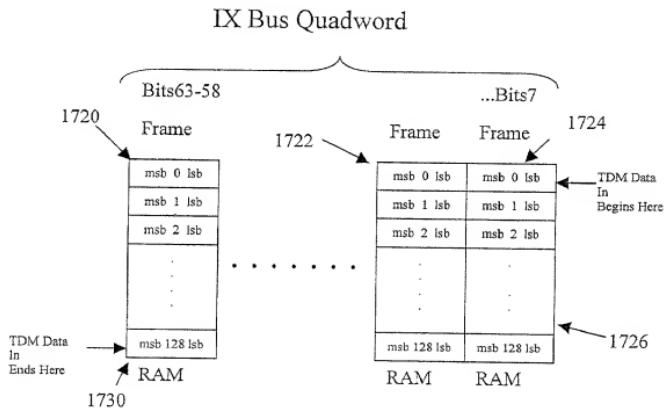


FIG. 17

RAM8	RAM7	RAM6	RAM5	RAM4	RAM3	RAM2	RAM1	RAM0	
Spare RAM Bank	F r a m e 7	F r a m e 7	F r a m e 6	F r a m e 5	F r a m e 4	F r a m e 3	F r a m e 2	F r a m e 1	F r a m e 0
H W Y I	H W Y 0	H W Y I	H W Y 0	H W Y I	H W Y 0	H W Y I	H W Y 0	H W Y I	H W Y 0

FIG. 18